

EE Capstone Design: EE401 (Winter)

Academic Term: Winter 2012

This document is an addendum (including modifications) to the preliminary EE401 syllabus provided at the start of EE400. The portions of that syllabus common to both of the courses still applies, and can be found at:

<http://www.ece.ualberta.ca/~ee401/lectures/syllabus2011-12.pdf>

There have been some key changes to the preliminary EE401 information contained in the prior document. This version supersedes what was communicated there.

EE 401: Engineering Design Project II

*2.5 (fi 6) (second term,1-0-3)

The second of two design courses that must be taken in the same academic year, in which student teams develop an electronic system or device from concept to working prototype. Emphasis is placed on continued execution of the project plan developed in E E 400. Formal interim and final reports are required; groups demonstrate and present their designs. Prerequisite: E E 400 in the preceding Fall term.

Lecture X50: ETLC E1-003, Tuesdays, 5PM-6PM

Labs: ETLC E3-001, M (X51), T(X52), W(X53), R (X54), 14:00-16:50

EE 401: Mark Allocation

Item	Weighting
Checkpoint Report I*	10%
Checkpoint Report II*	10%
Final Report	25%
Project Poster	10%
Final Presentation and Demonstration**	20%
Project Completion**	10%
Lab Book Records*	15%
TOTAL:	100%

* - Individual Evaluation

** - Provision for Individual Evaluation; group-evaluated except in extenuating circumstances.

All other items are group submissions, and the grade achieved on the item is assigned to all members of the group.

Don't Cheat Sheet

The University has made available a “Don’t Cheat Sheet” that aims to help instructors further clarify definitions of plagiarism, cheating, misrepresentation of facts, and participation in an offence. Please refer to

<http://www.governance.ualberta.ca/StudentAppeals/~media/University%20of%20Alberta/Administration/Office%20of%20the%20Vice-Provost/Governance/Documents/SA03/Dont%20Cheatsheet/Updated-Jan-5-2012.pdf>

to access this document.

Final Presentation/Demonstration Day

The department-wide Capstone Design Presentation and Demonstration Day, complete with an ongoing poster session is held on **Saturday, 14 April 2012** from 9AM until a time to be determined. Please keep your schedules clear for this event. The date is noted in the course schedule.

This day will see presentations from students in all disciplines: EE, CMPE (Software and Hardware), and Eng. Phys. Representatives from industry are asked to take part in the day and to provide feedback to groups.

Heads-up!

The course is highly back-end loaded. Please attempt to progress swiftly through execution of your project. In the past, groups that have completed their “product” early, leaving them to concentrate on documenting and presenting toward the end of the term, have done relatively well and without the stress that last-minute updates and modifications seem to cause.

EE 401 Schedule (Final)

Wk	M (H1)	T (H2)	W (H3)	R (H4)	F
1	09 Jan Classes Commence Lab Opens	10 Jan	11 Jan	12 Jan	13 Jan
2	16 Jan	17 Jan	18 Jan	19 Jan	20 Jan
3	23 Jan	24 Jan	25 Jan	26 Jan	27 Jan Checkpoint Report I Due
4	30 Jan	31 Jan	01 Feb	02 Feb	03 Feb
5	06 Feb	07 Feb	08 Feb	09 Feb	10 Feb
6	13 Feb	14 Feb	15 Feb	16 Feb	17 Feb
7	20 Feb University Closed Family Day Reading Break	21 Feb Reading Break	22 Feb Reading Break	23 Feb Reading Break	24 Feb Reading Break
8	27 Feb	28 Feb	29 Feb	01 Mar	02 Mar Checkpoint Report II Due
9	05 Mar	06 Mar	07 Mar	08 Mar	09 Mar
10	12 Mar	13 Mar	14 Mar	15 Mar	16 Mar
11	19 Mar	20 Mar	21 Mar	22 Mar	23 Mar
12	26 Mar	27 Mar	28 Mar	29 Mar	30 Mar
13	02 Apr	03 Apr	04 Apr	05 Apr Final Report Due	06 Apr University Closed Good Friday
14	09 Apr University Closed Easter Monday	10 Apr	11 Apr Project Posters Due	12 Apr	13 Apr Last Day of Classes Lab Books Due
					14 Apr Final Presentations/ Demonstrations/Poster Session: 9AM – TBD

Iron Ring